

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: FLETCHER, Thomas

Director of the U.S. Patent and Trademark Office

Application No.: 09/893,868

Group Art Unit: 2819

Examiner: Not assigned

Washington, D.C. 20231

Filed: June 29, 2001

For: CASCADED DOMINO FOUR-TO-TWO REDUCER CIRCUIT AND METHOD

INFORMATION DISCLOSURE STATEMENT

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APR 1 8 2002

Technology Center 2100

Sir:

Pursuant to 37 CFR § 1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom. The paragraphs marked below are applicable.

- 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date or the date of filing a CPA, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☐ 2. This Information Disclosure Statement is being filed with a Request for Continued Examination (RCE). No certification or fee is required.
- ☐ 3. This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection or Notice of Allowance.
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	b. I hereby certify that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than hree months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(2).
	c. Please debit Deposit Account No. 11-0600 in the amount of \$240.00 in payment of the fee under 37 CFR §1.17(p) to ensure consideration of the disclosed information. Two duplicate copies of this paper are attached.
months of Allow the Info 11-060 §1.17(i	This Information Disclosure Statement is being filed more than three after the U.S. filing date and after the mailing date of a Final Rejection or Notice vance, but before payment of the Issue Fee. Applicant(s) hereby petition(s) that rmation Disclosure Statement be considered. Please debit Deposit Account No. D in the amount of \$130.00 in payment of the petition fee under 37 CFR (1) to ensure consideration of the disclosed information. Two duplicate copies paper are attached.
	a. I hereby certify that each item of information contained in this information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(1).
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	Respectfully submitted,
	KENYON & KENYON

1500 K Street, NW Suite 700 Washington, DC 20005 (202) 220-4200

Date: <u>FCG 28,20.1</u>

(202) 220-4200 Fax: (202) 220-4201 Kenneth R. Corsello Registration No. 38,115 Approved for use through 10/31/2002. OMB 0651-0031
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INFORMATION DISCLOSURES

(use as many sheets as necessary)

1

First Named Inventor
Group Art Unit
Examiner Name
Attorney Docket Number

 Complete if Known

 Application Number
 09/893,868

 Filing Date
 June 29, 2001

 First Named Inventor
 FLETCHER, Thomas

 Group Art Unit
 2819

Not assigned

2207/11272

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			U.S. PATENT DO	DCUMENTS		
Examiner	Cite	Document Number	Publication Date	Name of Patentee or Applicant of Cited	Pages, Columns, Lines, Where	
Initials *	No.1	Number - Kind Code ² (if known)	MM-DD-YYYY	Document	Relevant Passages or Relevan Figures Appear	
		6,208,907 B1	03/27/2002	DURHAM et al		
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		FOREIGN PA	ATENT DOCU	MENTS		
Examin er Initials*	Cite No.1	Foreign Patent Document	Publication Date MM-DD- YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T⁵
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	OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS				
Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T 2		
		Yee et al, "Clock-Delayed Domino for Dynamic Circuit Design", IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 8, No. 4, August 2000, pp 425-430			
		Yee et al, "Clock-Delayed Domino for Adder and Combinational Logic Design", IEEE, 1063-6404/96, pp 332-337, 1996			
_		Jung, Perepelitsa, Sobelman, "Time Borrowing in High-Speed Functional Units Using Skew-Tolerant Domino Circuits," Proceedings, IEEE International Symposium on Circuits and Systems, pp. V-641 - V-644, 2000			
		Presentation by Carl Sechen dated March 17, 2000.			
		Taub, <i>Digital Circuits and Microprocessors</i> , pages 205-212, McGraw-Hill, 1982			

Examiner	Date	
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